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	Application Number		10715459			
	Filing Date		2003-11-19			
INFORMATION DISCLOSURE	First Named Inventor   Curtis Davis					
STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Art Unit		2183			
(Not for Submission under 57 Of K 1.55)	Examiner Name	Keith	E. Vicary			
	Attorney Docket Numb	er	NVDA/AG-08-0112-US			

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## 10715459 Application Number Filing Date 2003-11-19 INFORMATION DISCLOSURE First Named Inventor **Curtis Davis** STATEMENT BY APPLICANT Art Unit 2183 (Not for submission under 37 CFR 1.99) **Examiner Name** Keith E. Vicary Attorney Docket Number NVDA/AG-08-0112-US SUZUAZU et al. "A Microprocessor with a 128-Bit CPU, Ten Floating MAC's, Four Floating-Point Dividers, and an MPEG-2 Decoder," IEEE Journal of Solid State Circuits, Vol. 34, November 1999, pp. 1608-1618. 2 Office Action, Ser. No. 10/715,400, dated February 16, 2010. If you wish to add additional non-patent literature document citation information please click the Add button **EXAMINER SIGNATURE Examiner Signature Date Considered** \*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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